

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

PUBLICATION NUMBER : 04055437  
PUBLICATION DATE : 24-02-92

APPLICATION DATE : 26-06-90  
APPLICATION NUMBER : 02167782

APPLICANT : MATSUSHITA ELECTRIC WORKS LTD;

INVENTOR : KITSUTA YOSHIHIRO;

INT.CL. : C08J 5/24 H05K 1/03 // C08L 27:00

TITLE : PREPREG FOR LAMINATED BOARD WITH LOW DIELECTRIC CONSTANT

ABSTRACT : PURPOSE: To obtain the subject prepreg, useful as a substrate material for printing wiring boards and excellent in low dielectric constant and low thermal expansion coefficient at a low cost by impregnating a sheetlike substrate with a dispersion of a fluororesin and specific glass spheres and drying the impregnated substrate.

CONSTITUTION: The objective prepreg is obtained by impregnating a substrate such as a woven or nonwoven fabric preferably composed of glass fiber or aramid fiber with a homogeneous dispersion of a fluororesin such as preferably polytetrafluoroethylene or tetrafluoroethylene-hexafluoropropylene copolymer and preferably hollow glass spheres (in an amount of preferably 15-50 wt.% based on the total solid content in the prepreg), preferably coated with a silane coating agent and having 1.5-20 µm average grain diameter and drying the resultant impregnated substrate.

COPYRIGHT: (C)1992,JPO&Japio